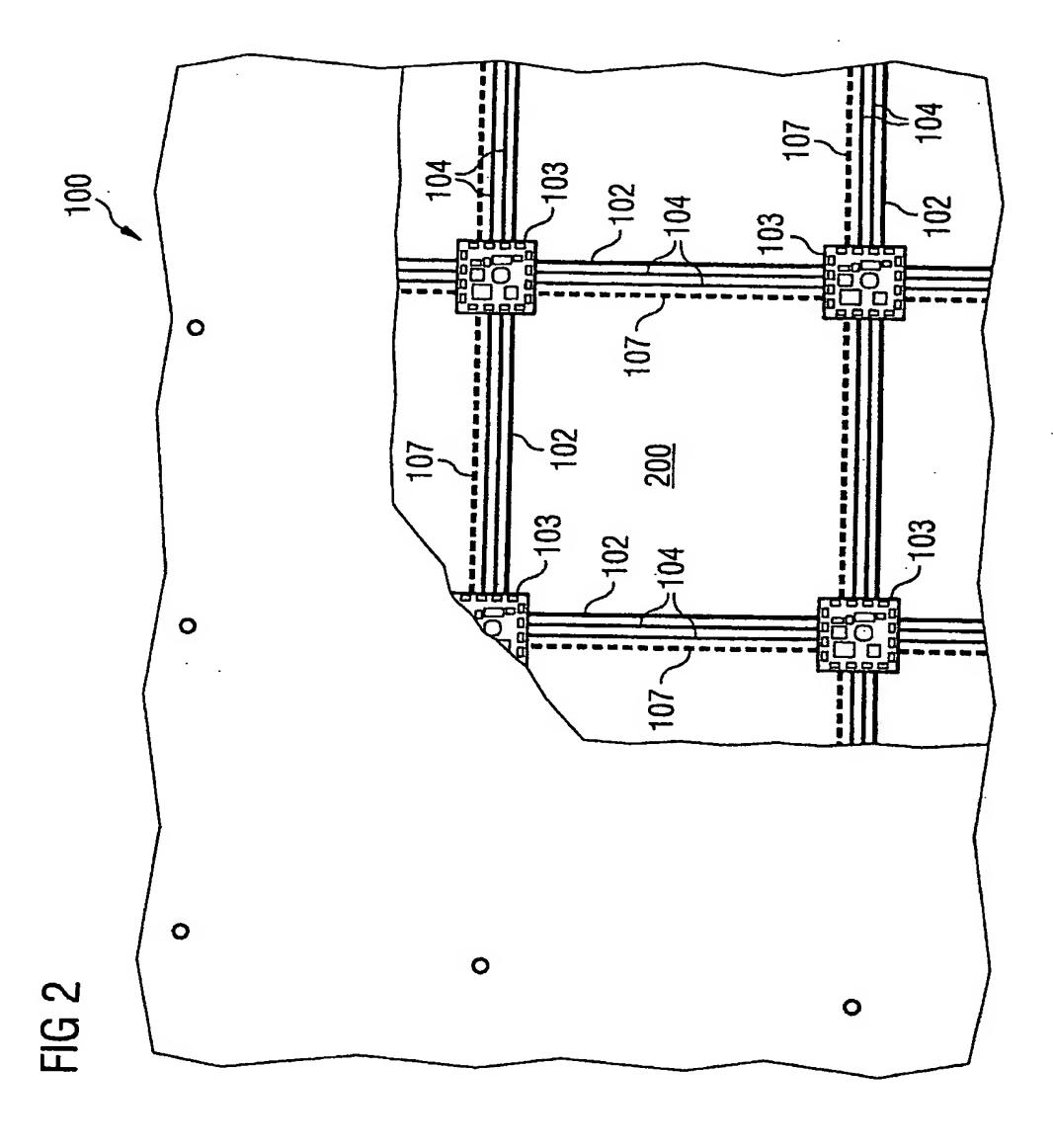
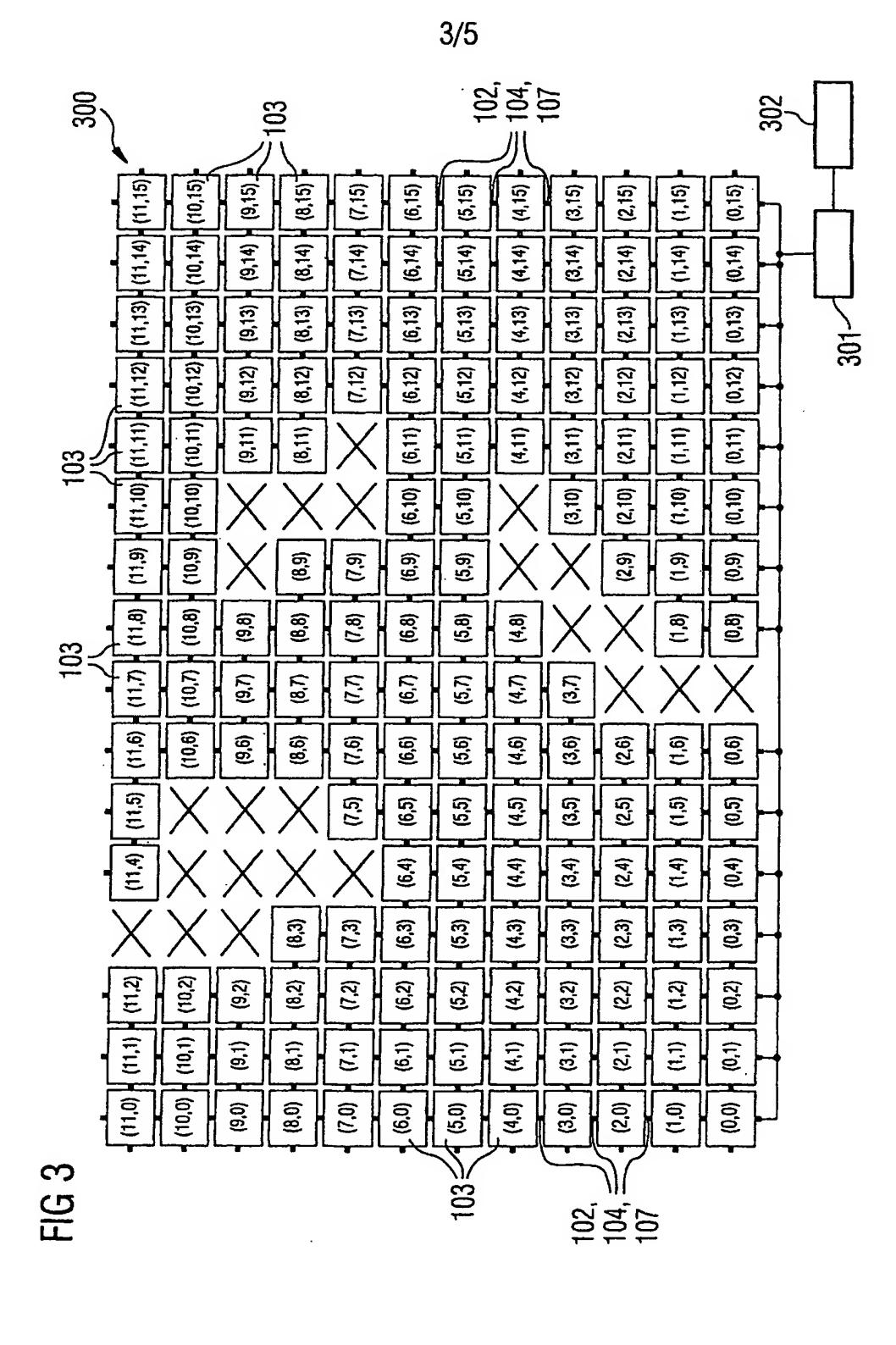


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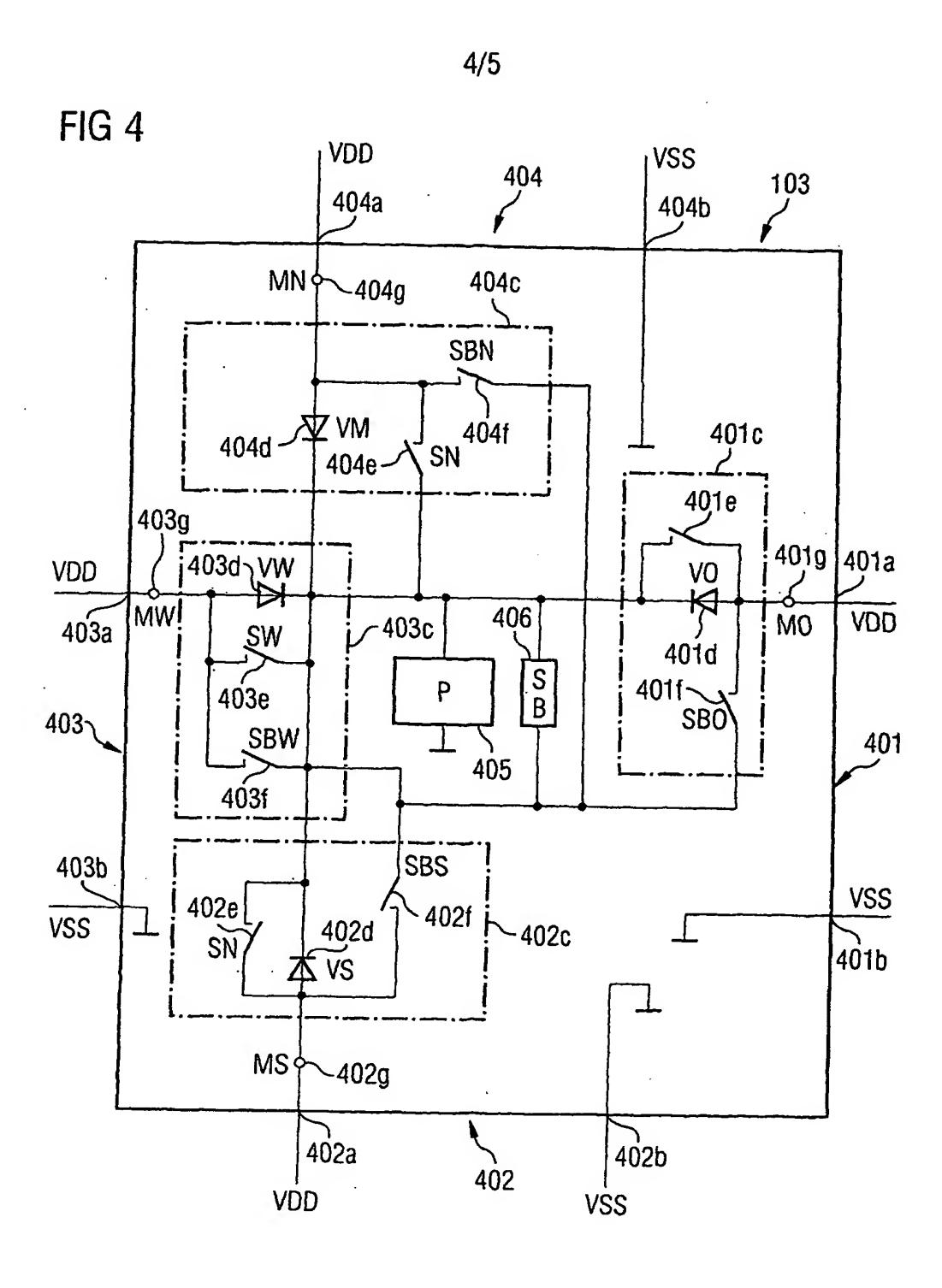
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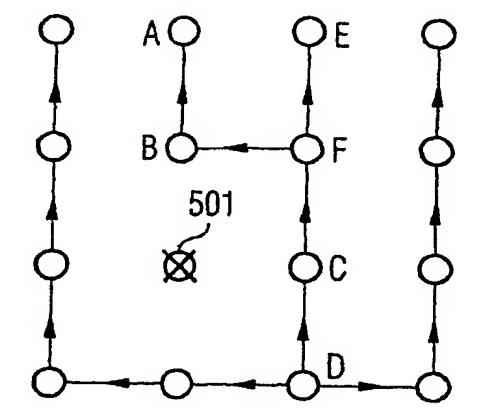


WO 2005/031550 PCT/DE2004/002059



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FIG 5

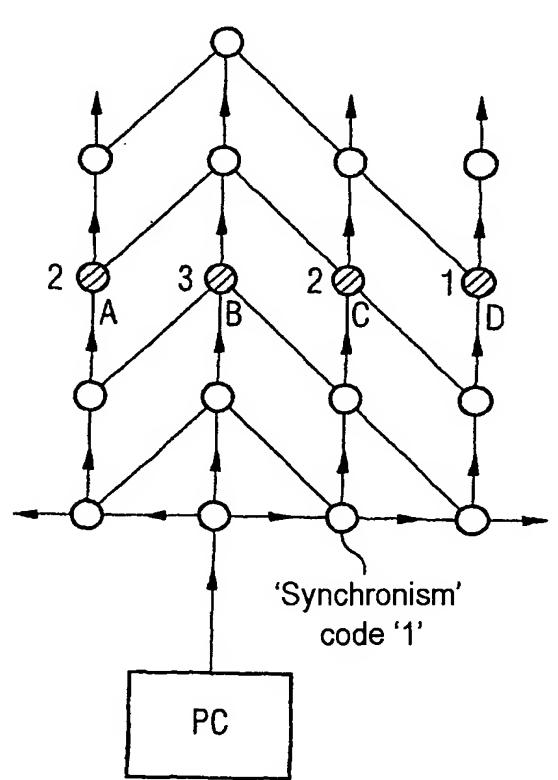


O Processor

Routing path

Processor defective

FIG 6



Output "LED" on

X Time code